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		<b>.</b>	OL COURT	Application Number			
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STAT	EMENT RY	/ Δ	PPLICANT	First Named Inventor	Alina Deutsch		
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Sheet	1	of	1	Attorney Docket Number	YOR920040041US1 (163-30)		

OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	1			
		RUI WANG, OMAR WING; Transient Analysis of Dispersive VLSI Interconnects Terminated in Nonlinear Loads; IEEE Transactions on Computer-Aided Design, Vol. 11 No. 10 October 1992; pages 1258-1277				
		ALBERT E. RUEHLI AND THOMAS A. JOHNSON; Circuit Analysis Computing of Semiconductor Packages and Systems; Wiley Encyclopedia of Electrical and Electronics Engineering, Vol. 3; pages 339-354				
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